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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/612,963	07/07/2003	Shin-Tai Lo	2450-0494P	4750

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EXAMINER

TRAN, THUY V

ART UNIT PAPER NUMBER

2821

DATE MAILED: 02/09/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/612,963	Applicant(s) LO ET AL.	
	Examiner Thuy V. Tran	Art Unit 2821	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 03 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on amendment filed 11/22/2004.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 07 July 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

This is a response to the Applicants' amendment submitted on November 22nd, 2004. In virtue of this amendment:

- Claims 1-5 are originally filed;
- Claims 6-7 are newly added; and thus,
- Claims 1-7 are now presented in the instant application.

Claim Objections/ Minor Informalities

1. Claim 5 is objected to because of the following informalities:

Line 14, "the" should be changed to --a--; and "an" should be changed to --a--; and

Line 15, "an" should be changed to --a--;

Line 18, "the" should be deleted; and --of the driver units-- should be inserted between "voltages" and "are".

Appropriate correction is required.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

Art Unit: 2821

3. Claims 1-7 are rejected under 35 U.S.C. 102(e) as being anticipated by Yamazaki et al. (U.S. Patent No. 6,528,951).

With respect to claim 1, Yamazaki et al. discloses, in Figs. 4-7, an apparatus for generating uniform images of an active matrix organic light emitting diode display device (see col. 1, line 22) comprising a plurality of pixel devices [104]; each of the pixel devices comprises (1) a switch unit [1701] having two input ends and an output end (connecting to [1704] and [1702] (see Fig. 6); the two input ends connect respectively to a data line [1706] and a scan line [1705], (2) a storage unit [1704] having one end connecting to a supply line [1707] and another end connecting to the output end of the switch unit [1701], (3) a driver unit [1702] having two input ends and an output end (connecting to [1703]; see Fig. 6); one input end connects to the supply line [1707] and another input end connects to the output end of the switch unit [1701], and (4) an OLED [1703] having an anode and a cathode (see col. 3, line 1); the anode is connected to the output end of the driver unit [1702] (see col. 3, lines 3-4) and the cathode is connected to a power supply (via switch [116]; see Figs. 4 and 5), wherein the power supply provides a voltage (in the display period; see col. 3, lines 57-60) to control a shift of a loading curve of the driver unit by raising an electric potential of the output end of the driver unit (since the voltage provided by the power supply is positive; see Fig. 5), thereby minimizing a fluctuation of an output current of the driver unit among the plurality of pixel devices.

With respect to claim 2, Yamazaki et al. discloses that the switch unit [1701] is a thin film transistor (TFT; see col. 4, line 13).

With respect to claim 3, Yamazaki et al. discloses that the driver unit [1702] is a thin film transistor (TFT; see col. 4, line 13).

Art Unit: 2821

With respect to claim 4, Yamazaki et al. discloses that the storage unit [1704] includes a capacitor (see col. 4, line 14).

With respect to claim 5, Yamazaki et al. discloses, in Figs. 4-7, an apparatus and a corresponding method for generating uniform images of an active matrix organic light emitting diode display device which comprises a plurality of pixel devices [104]; each of the pixel devices comprises a driver unit [1702] to drive an OLED [1703] to display (see Fig. 6); the method comprises the steps of (1) raising an electric potential of a drain electrode of the driver unit [1702] (in the display period, the switch [116] connects [Vb1, ..., Vby] to the power supply; see Fig. 5; col. 3, lines 15-17 and 57-60) to shift a loading curve of the driver unit; and (2) keeping a voltage difference of a source electrode and a gate electrode unchanged (during the writing period; see col. 3, lines 54-56) to minimize a fluctuation of an output current of the driver unit among the plurality of pixel devices when threshold voltages of the driver units are different due to characteristic variations of the driver units of the plurality of pixel devices.

With respect to claim 6, Yamazaki et al. discloses that the voltage is a positive voltage (provided by the power supply connected to the switch 116; see Figs. 4 and 5).

With respect to claim 7, Yamazaki et al. discloses that the step of raising the electric potential of the drain electrode comprises applying a positive voltage to a cathode of the OLED (in the display period, the switch [116] connects the OLED's cathode to the power supply; see col. 3, lines 56-60) to reduce the voltage difference between the source electrode of the driver unit and the drain electrode of the driver unit during operation.

Remarks and conclusion

Art Unit: 2821

4. Applicant's arguments in page 7 of the amendment submitted on 11/22/2004 with respect to amended claims 1 and 5 have been considered but are moot in view of the new ground(s) of rejection.

As clearly addressed above, Yamazaki et al. discloses all the limitations recited in claims 1-7; and therefore, claims 1-7 are rejected under 35 USC 102(e) as being anticipated by this reference.

5. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

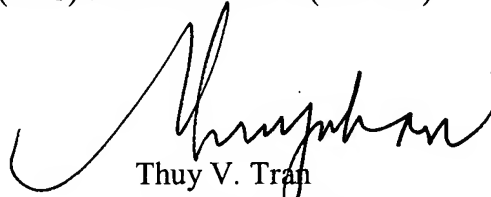
Inquiry

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuy V. Tran whose telephone number is (571) 272-1828. The examiner can normally be reached on M-F (8:00 AM -5:00 PM).

Art Unit: 2821

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Don Wong can be reached on (571) 272-1834. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A handwritten signature in black ink, appearing to read 'Thuy V. Tran', is positioned above the printed name and title.

Thuy V. Tran
Primary Examiner
Art Unit 2821

02/06/2005